

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>1</b>	<b>L1</b>	<b>1518</b>	<b>(361/767,768,769,771,803).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>2</b>	<b>L2</b>	<b>1900</b>	<b>(174/262-264,266).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>3</b>	<b>L3</b>	<b>5787</b>	<b>(257/698,723,737,738,772,778,779,785).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>4</b>	<b>L4</b>	<b>2326</b>	<b>(29/830,840).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>5</b>	<b>L5</b>	<b>1063 3</b>	<b>1 2 3 4</b>	<b>USP AT; US-P GPU B</b>
<b>6</b>	<b>L6</b>	<b>826</b>	<b>5 and @pd&gt;=20031118</b>	<b>USP AT; US-P GPU B</b>
<b>7</b>	<b>L7</b>	<b>429</b>	<b>6 and ((through adj hole\$1) vias (conductive adj via\$1) (via adj hole\$1))</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>8</b>	<b>L8</b>	<b>95</b>	<b>7 and (groove\$1 channel\$1 trench\$2)</b>	<b>USP AT; US-P GPU B</b>
<b>9</b>	<b>L9</b>	<b>334</b>	<b>7 not 8</b>	<b>USP AT; US-P GPU B</b>
<b>10</b>	<b>L10</b>	<b>17</b>	<b>9 and (castellation\$1 notch\$2 (half adj moon) semicircular)</b>	<b>USP AT; US-P GPU B</b>
<b>11</b>	<b>L11</b>	<b>317</b>	<b>9 not 10</b>	<b>USP AT; US-P GPU B</b>
<b>12</b>	<b>L12</b>	<b>114</b>	<b>11 and (interposer\$1 carrier\$1 translator\$1 (adapter adj board\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>13</b>	<b>L13</b>	<b>203</b>	<b>11 not 12</b>	<b>USP AT; US-P GPU B</b>
<b>14</b>	<b>L14</b>	<b>397</b>	<b>6 not 7</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>15</b>	<b>L15</b>	<b>22</b>	<b>14 and (castellation\$1 notch\$2 (half adj moon) semicircular)</b>	<b>USP AT; US-P GPU B</b>
<b>16</b>	<b>L16</b>	<b>375</b>	<b>14 not 15</b>	<b>USP AT; US-P GPU B</b>
<b>17</b>	<b>L17</b>	<b>73</b>	<b>16 and (groove\$1 channel\$1 trench\$2)</b>	<b>USP AT; US-P GPU B</b>
<b>18</b>	<b>L18</b>	<b>302</b>	<b>16 not 17</b>	<b>USP AT; US-P GPU B</b>
<b>19</b>	<b>L19</b>	<b>97</b>	<b>18 and (interposer\$1 carrier\$1 translator\$1 (adapter adj board\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>20</b>	<b>L21</b>	<b>0</b>	<b>20 and ((through adj hole\$1) vias (conductive adj via\$1) (via adj hole\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>21</b>	<b>L20</b>	<b>205</b>	<b>18 not 19</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>22</b>	<b>L22</b>	<b>1282</b>	<b>174/255.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>23</b>	<b>L23</b>	<b>61</b>	<b>22 and (castellation\$1 notch\$2 (half adj moon) semicircular)</b>	<b>USP AT; US-P GPU B</b>
<b>24</b>	<b>L24</b>	<b>59</b>	<b>23 not 6</b>	<b>USP AT; US-P GPU B</b>
<b>25</b>	<b>L25</b>	<b>239</b>	<b>22 and (groove\$1 channel\$1 trench\$2)</b>	<b>USP AT; US-P GPU B</b>
<b>26</b>	<b>L26</b>	<b>212</b>	<b>25 not (6 24)</b>	<b>USP AT; US-P GPU B</b>
<b>27</b>	<b>L27</b>	<b>346</b>	<b>22 and (interposer\$1 carrier\$1 translator\$1 (adapter adj board\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>28</b>	<b>L28</b>	<b>263</b>	<b>27 not (6 24 26)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>29</b>	<b>L29</b>	<b>779</b>	<b>22 and ((through adj hole\$1) vias (conductive adj via\$1) (via adj hole\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>30</b>	<b>L30</b>	<b>392</b>	<b>29 not (6 24 26 28)</b>	<b>USP AT; US-P GPU B</b>
<b>31</b>	<b>L31</b>	<b>867</b>	<b>29/852.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>32</b>	<b>L32</b>	<b>37</b>	<b>31 and (castellation\$1 notch\$2 (half adj moon) semicircular)</b>	<b>USP AT; US-P GPU B</b>
<b>33</b>	<b>L33</b>	<b>830</b>	<b>31 not 32</b>	<b>USP AT; US-P GPU B</b>
<b>34</b>	<b>L34</b>	<b>164</b>	<b>31 and (groove\$1 channel\$1 trench\$2)</b>	<b>USP AT; US-P GPU B</b>
<b>35</b>	<b>L35</b>	<b>136</b>	<b>34 not (6 24 26 28 30 32)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>36</b>	<b>L36</b>	<b>238</b>	<b>31 and (interposer\$1 carrier\$1 translator\$1 (adapter adj board\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>37</b>	<b>L37</b>	<b>143</b>	<b>36 not (6 24 26 28 30 32 35)</b>	<b>USP AT; US-P GPU B</b>
<b>38</b>	<b>L38</b>	<b>514</b>	<b>31 not (32 34 36)</b>	<b>USP AT; US-P GPU B</b>
<b>39</b>	<b>L39</b>	<b>397</b>	<b>38 and ((through adj hole\$1) vias (conductive adj via\$1) (via adj hole\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>40</b>	<b>L40</b>	<b>117</b>	<b>38 not 39</b>	<b>USP AT; US-P GPU B</b>